

FIG. 1

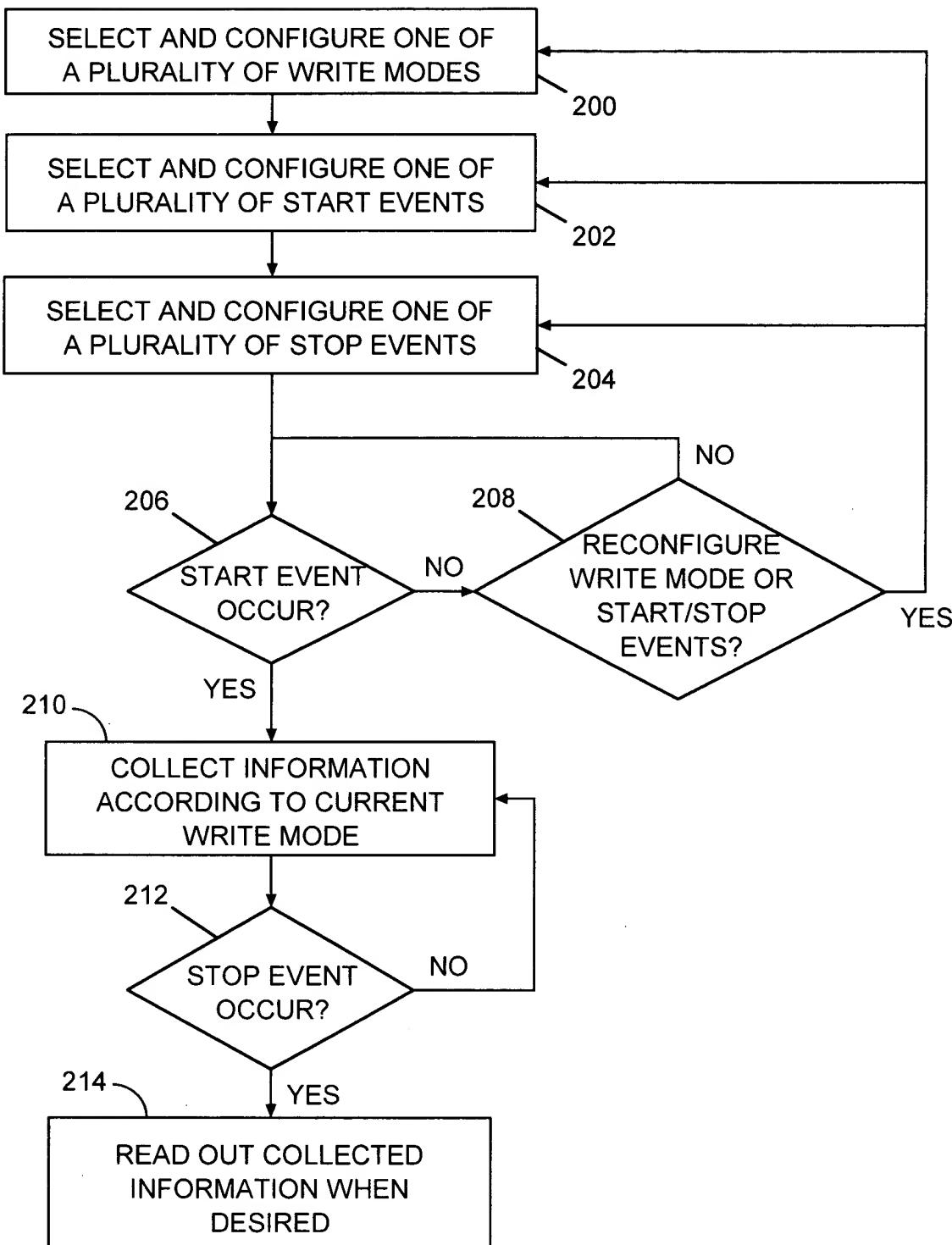


FIG. 2

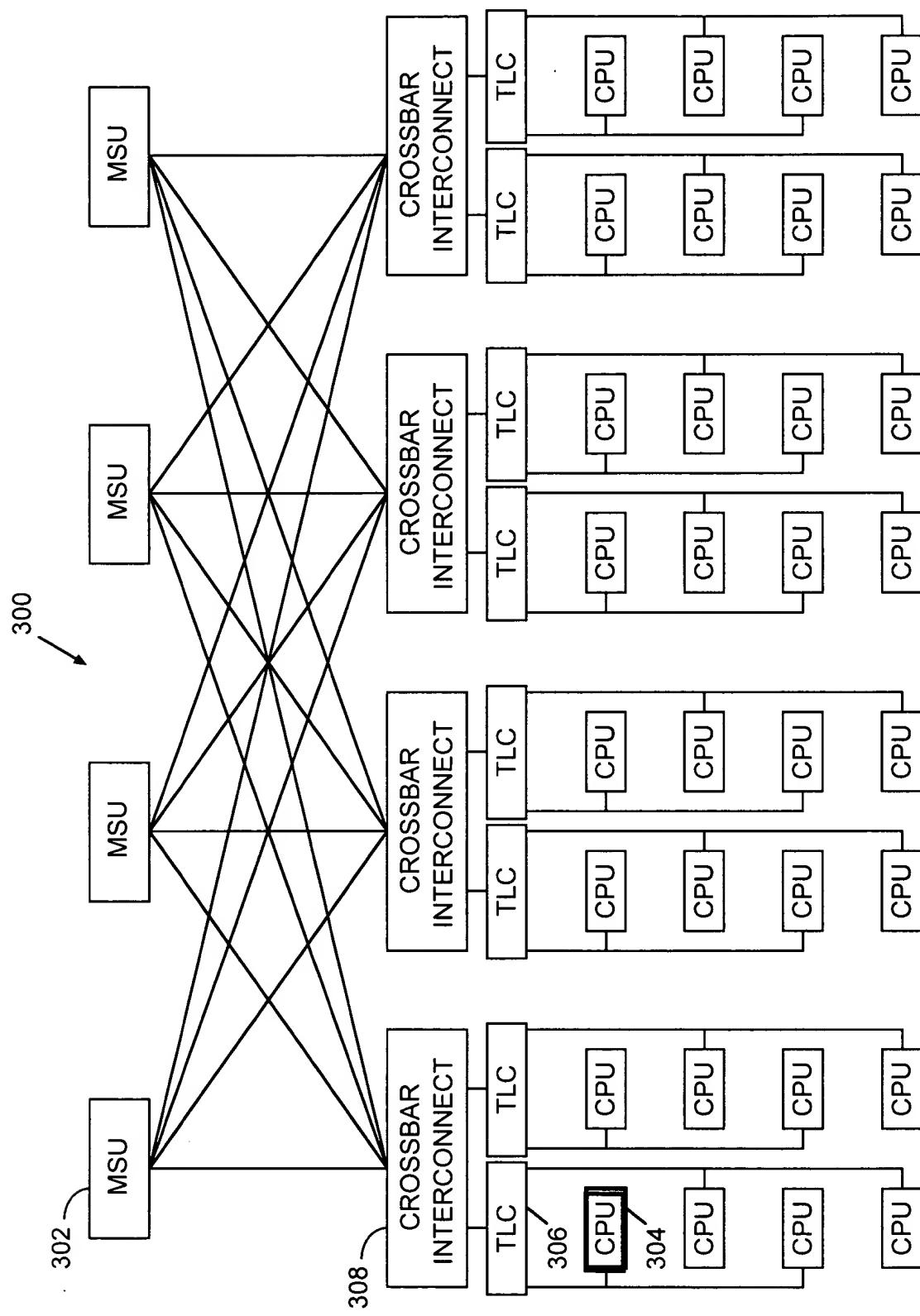


FIG. 3

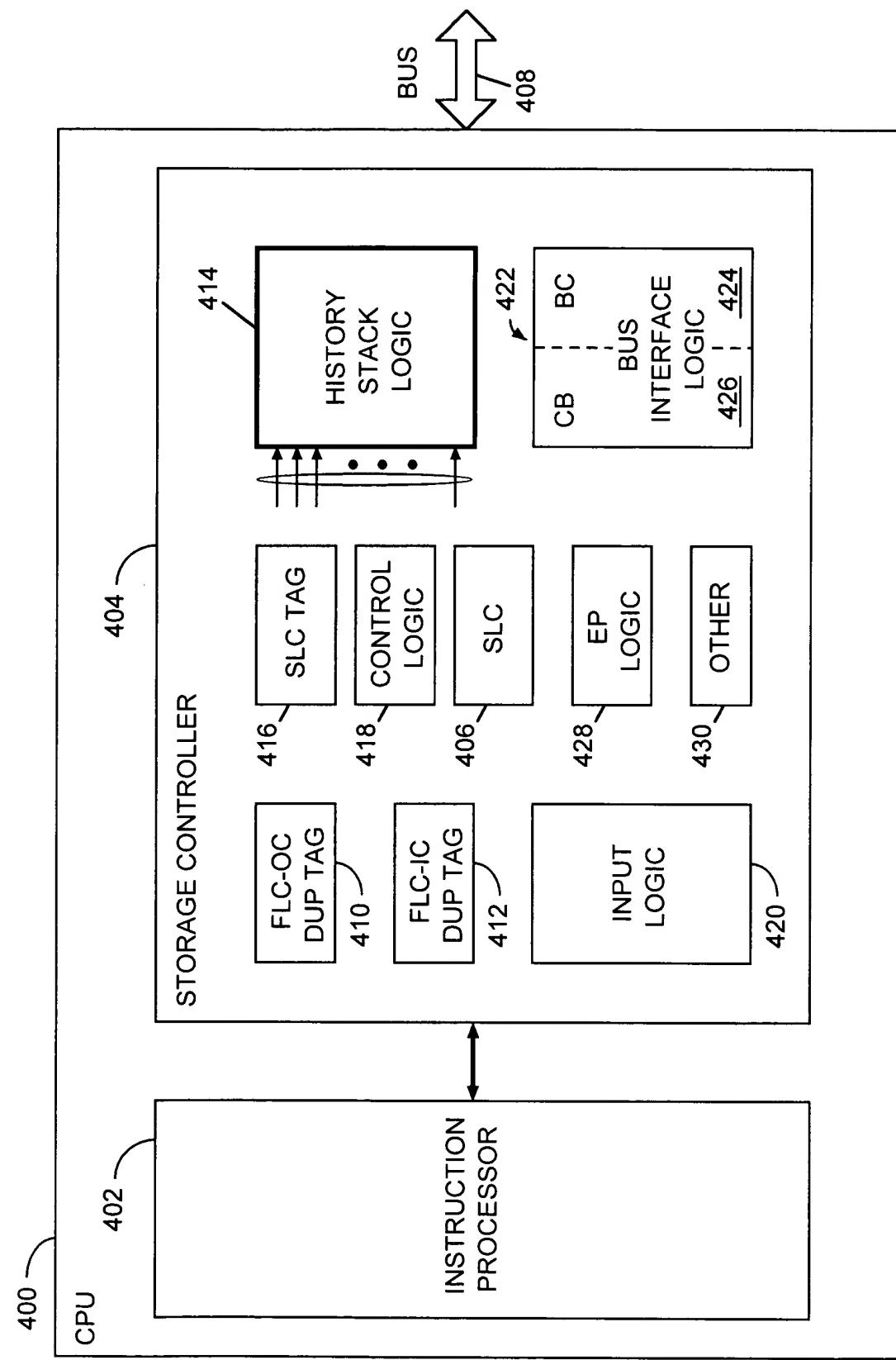
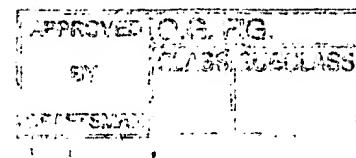


FIG. 4

502 500 504

506	WRITE MODE	REGISTER BITS
508	NORMAL	1 X X
510	CYCLE	0 1 1
512	FUNCTION/ADDRESS	0 0 0
514	DUPLICATE TAG	0 0 1
	NORMAL OR DUPLICATE TAG	0 1 0

FIG. 5

602 600 604

606	START FEATURE	REGISTER BITS
608	ALWAYS ON	0 0
610	START ON F/A COMPARE	0 1
612	START ON IP SIGNAL	1 0
	UNDEFINED	1 1

FIG. 6

	STOP FEATURE	REGISTER BITS
706	NO STOP	0 0 0
708	STOP ON F/A COMPARE	0 0 1
710	STOP ON IP SIGNAL	0 1 0
712	STOP BEFORE OVERWRITE	0 1 1
714	STOP ON EP DETECTED CRITICAL ERROR	1 0 0
716	STOP ON EP DETECTED PASSIVE ERROR	1 0 1
718	STOP ON NO REQUEST FROM IP	1 1 0
720	STOP ON NO REQUEST FROM BUS	1 1 1

FIG. 7

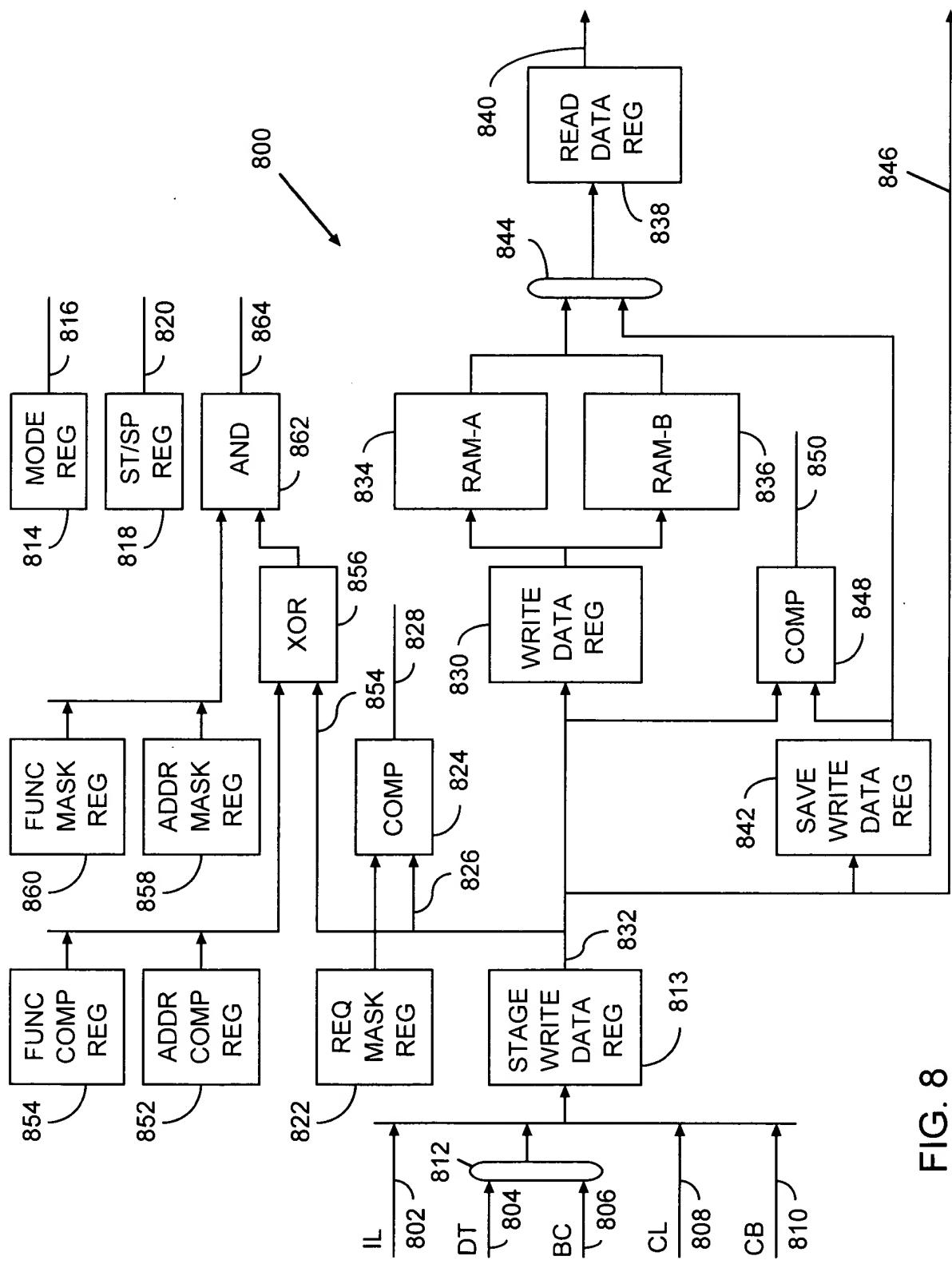
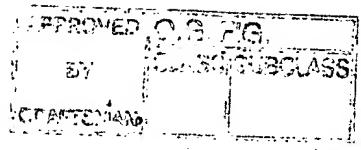


FIG. 8

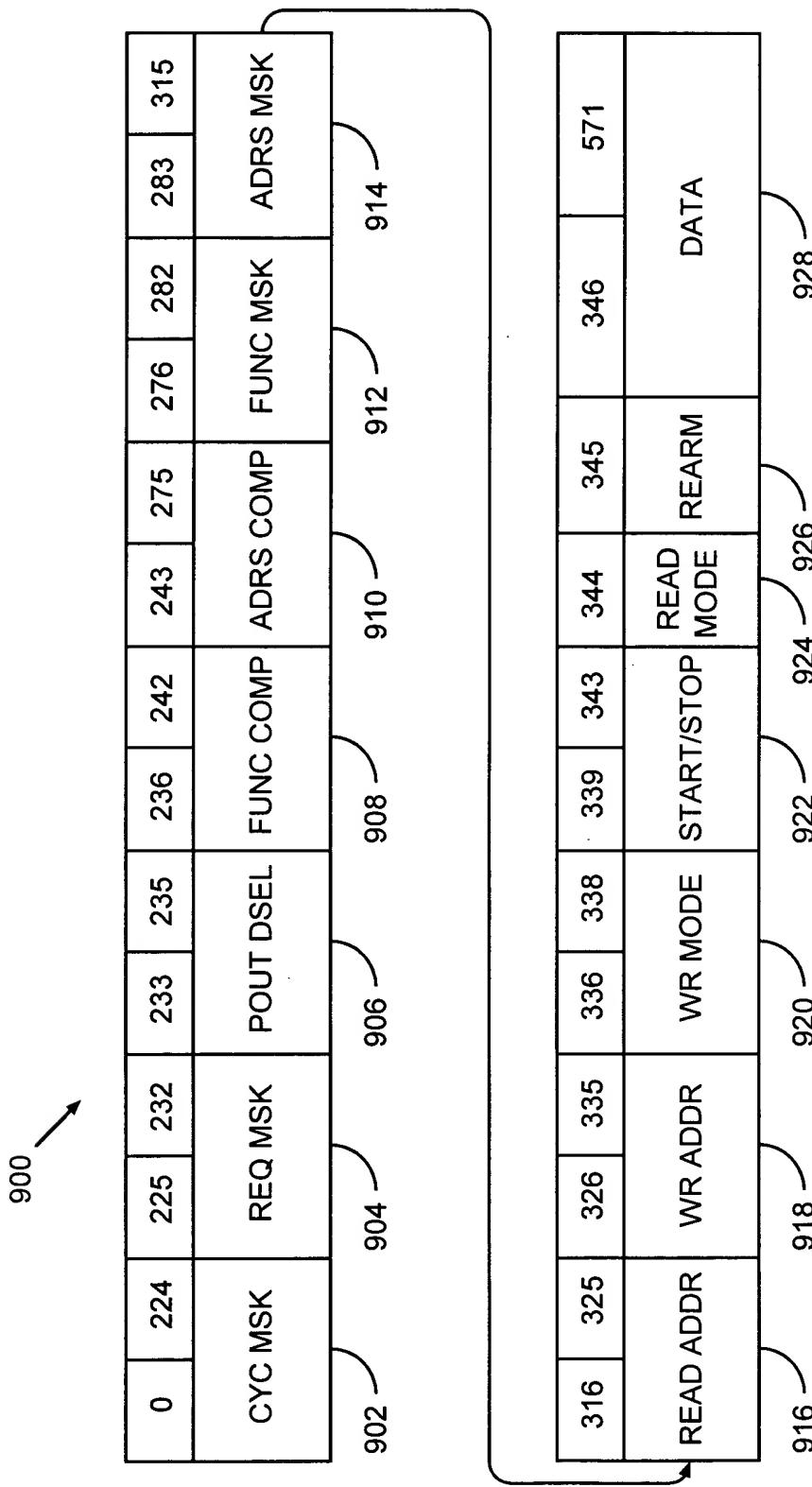
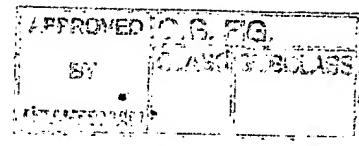


FIG. 9